

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Juan G. Revilla, et al. Art Unit: 2186
Serial No.: 10/040,920 Examiner: Tuan V. Thai
Filed : December 28, 2001 Assignee: Intel Corporation
Title : METHOD FOR EXTENDING THE LOCAL MEMORY ADDRESS SPACE
 OF A PROCESSOR

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

This information disclosure statement is being submitted pursuant to 37 C.F.R. 1.97(i). Applicant submits the reference listed on the attached form PTO-1449.

Respectfully submitted,

Date: December 20, 2006

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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-566001	Application No. 10/040,920
Information Disclosure Statement by Applicant (Use several sheets if necessary)		Applicant Juan G. Revilla, et al.	
		Filing Date December 28, 2001	Group Art Unit 2186

(37 CFR §1.98(b))

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	4,797,814	1/10/1989	Brenza			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AQ	
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	